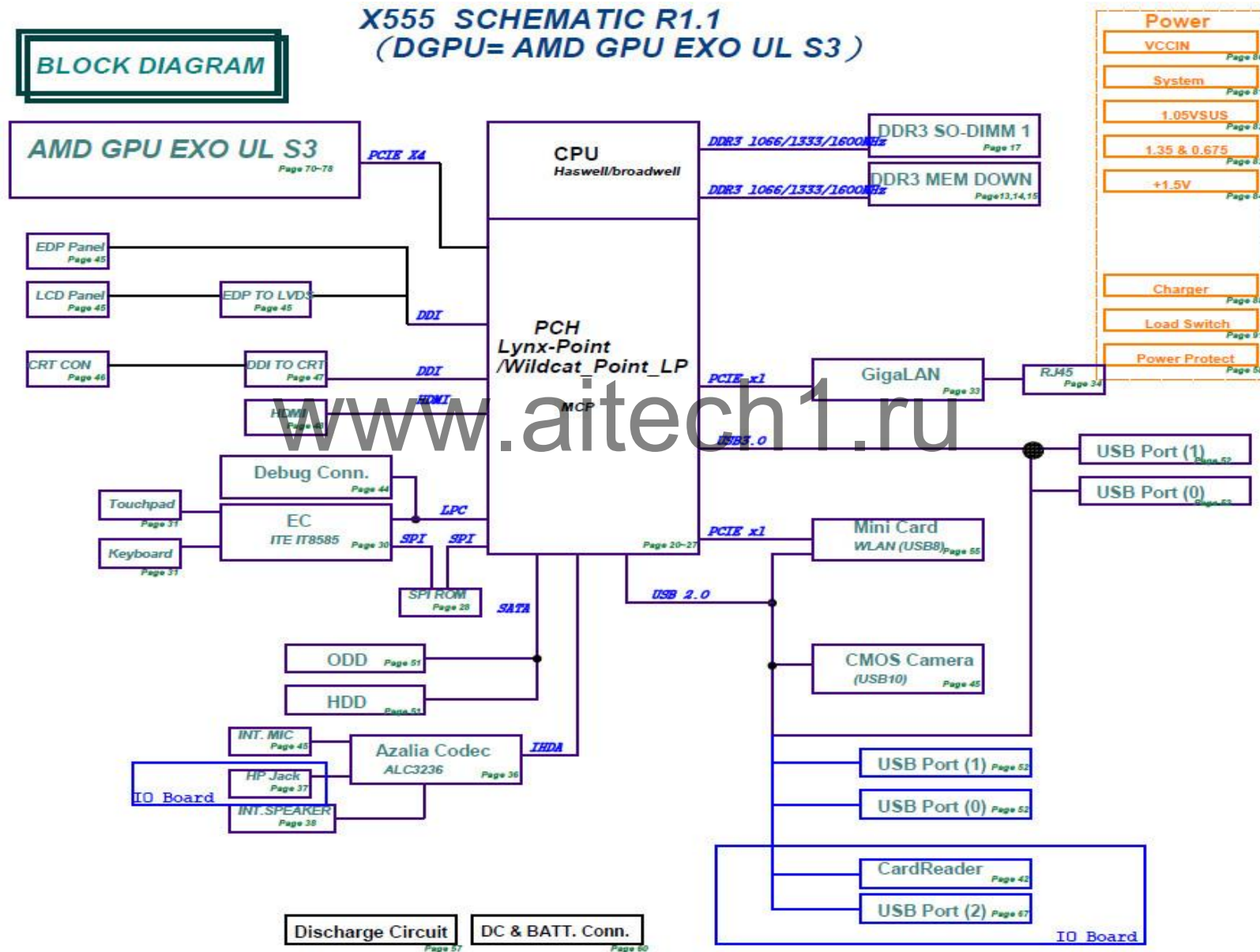
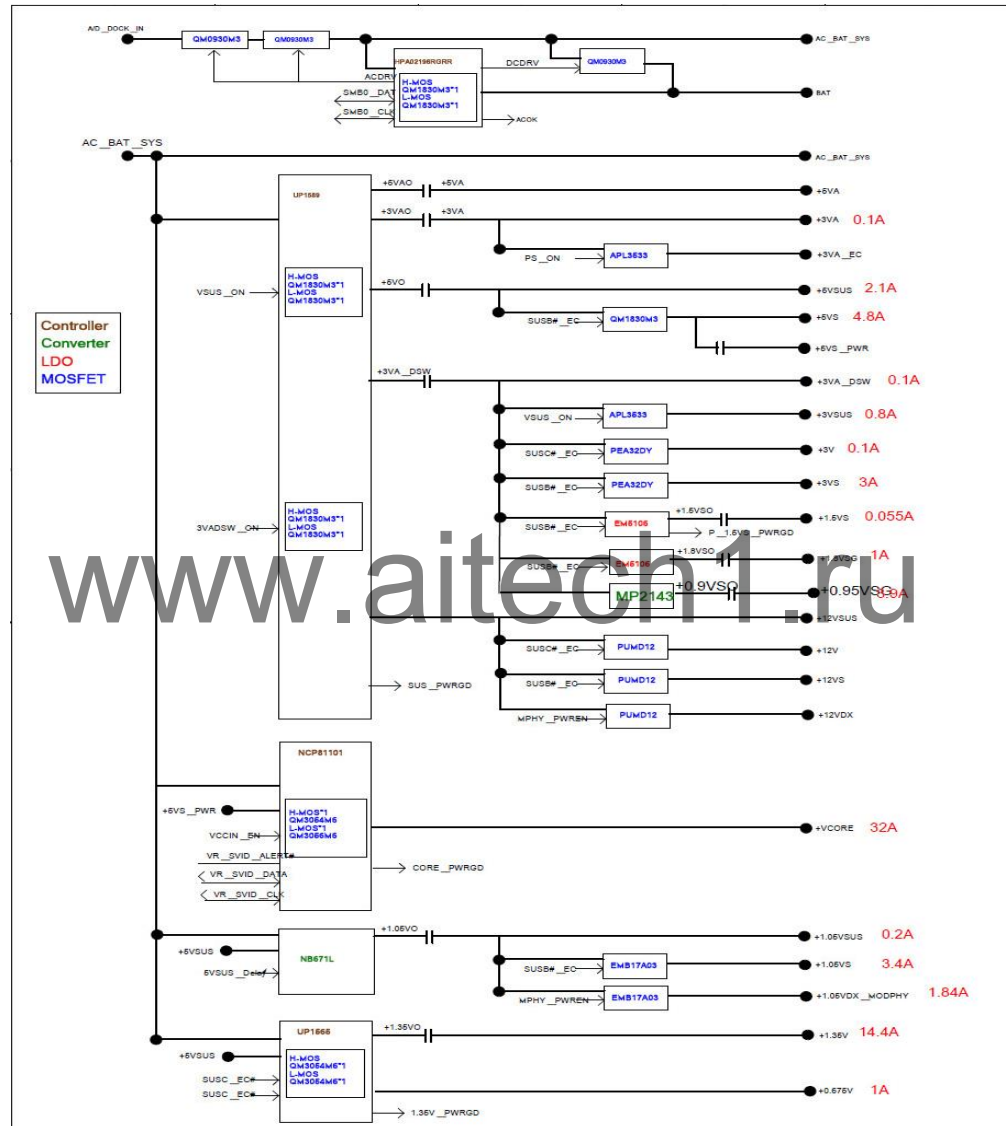


## BLOCK DIAGRAM

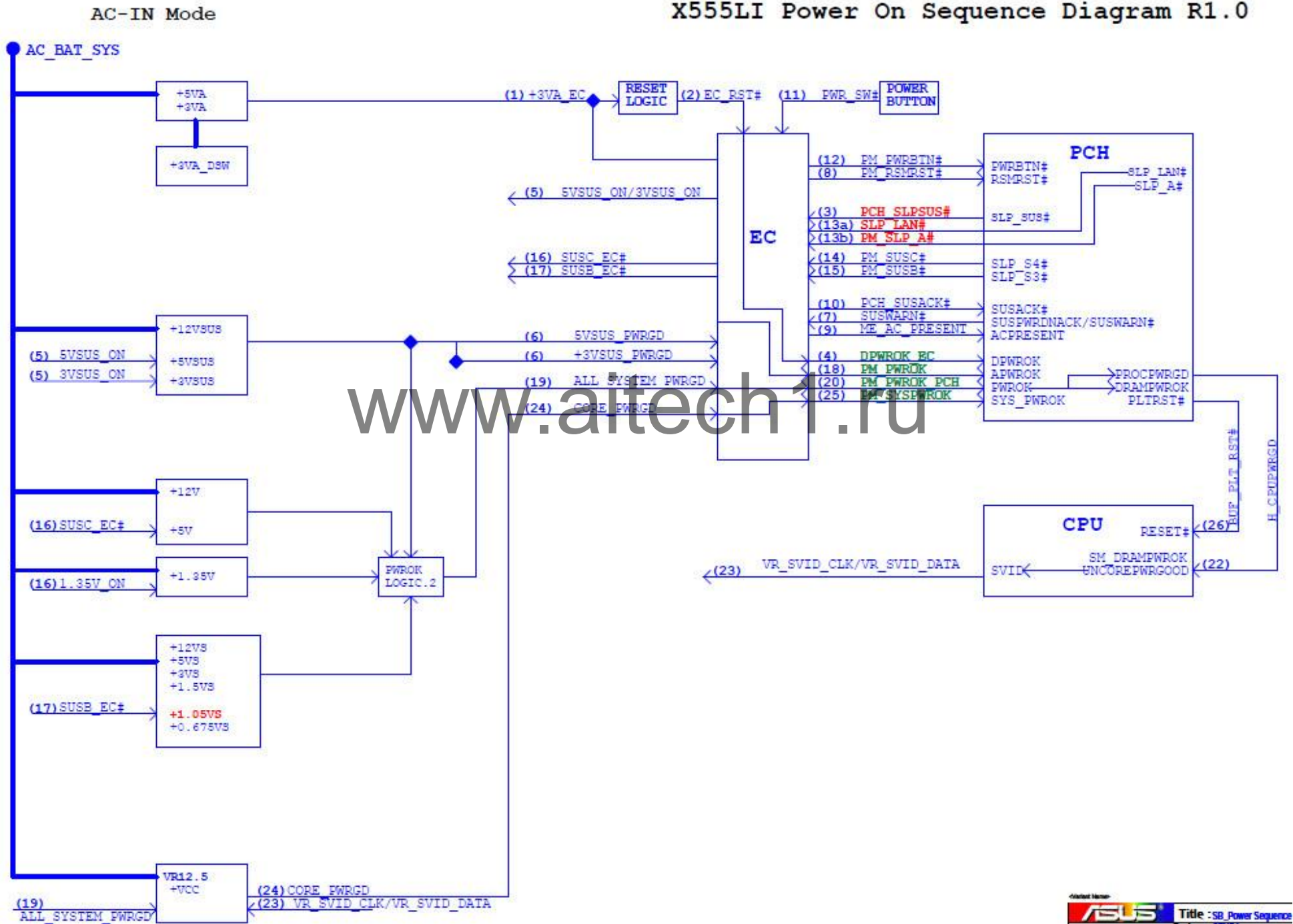


# POWER FLOW



# POWER ON SEQUENCE

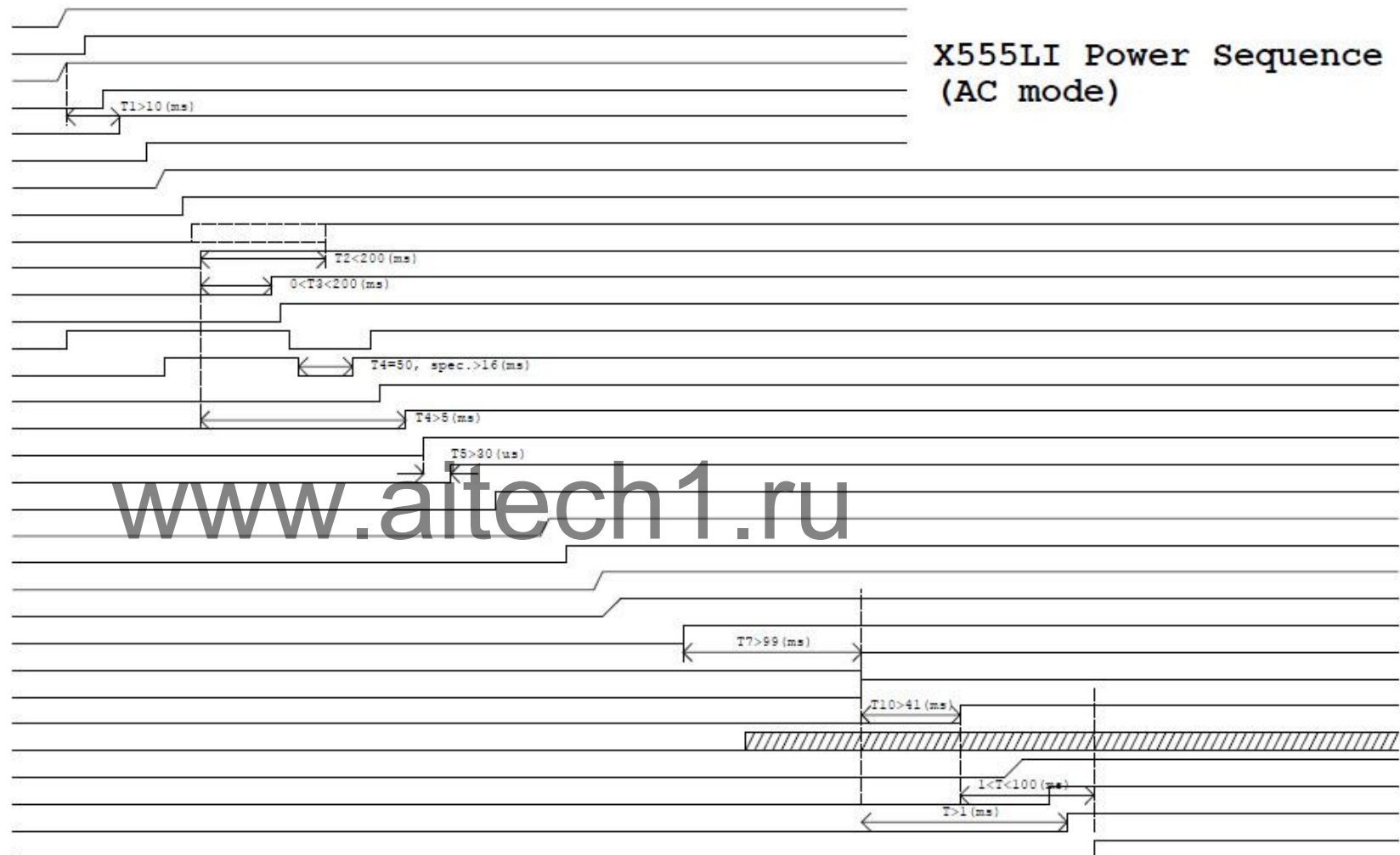
X555LI Power On Sequence Diagram R1.0



# POWER ON SEQUENCE (AC Mode)

## AC-IN Mode

- 1 +3VA/+5VA/+3VA\_EC
- 2 EC\_RST#
- +3VA\_DSW
- 3 PCH\_SLP\_SUS#
- 4 DPWROK\_EC
- 5 SVSUS\_ON/3VSUS\_ON
- +5VSUS/+3VSUS
- SVSUS\_PWRGD/+3VSUS\_PWRGD
- 7 SUSWARN#
- 8 PM\_RSMRST#
- 9 ME\_AC\_PRESENT
- 10 PCH\_SUSACK#
- 11 PWR\_SW#
- 12 PM\_PWRBTN#
- 13(a) SLP\_LAN#
- 13(b) PM\_SLP\_A#
- 14 PM\_SUSC#
- 15 PM\_SUSB#
- 16 SUSC\_EC#
- +5V/1.35V
- 17 SUSB\_EC#
- +5VS/+3VS/+1.5VS
- +3V5G/+1.8V5G/+1.35V5G/+0.95V5G
- 23 ALL\_SYSTEM\_PWRGD
- 24 PM\_PCHPWROK
- 25 PM\_SYSPWROK
- 27 H\_CPUPWRGD
- 28 VR\_SVID\_CLK/VR\_SVID\_DATA
- +VOCIN
- 29 CORE\_PWRGD
- 30 PCH\_SUS\_STAT#
- 31 BUF\_PLT\_RST#

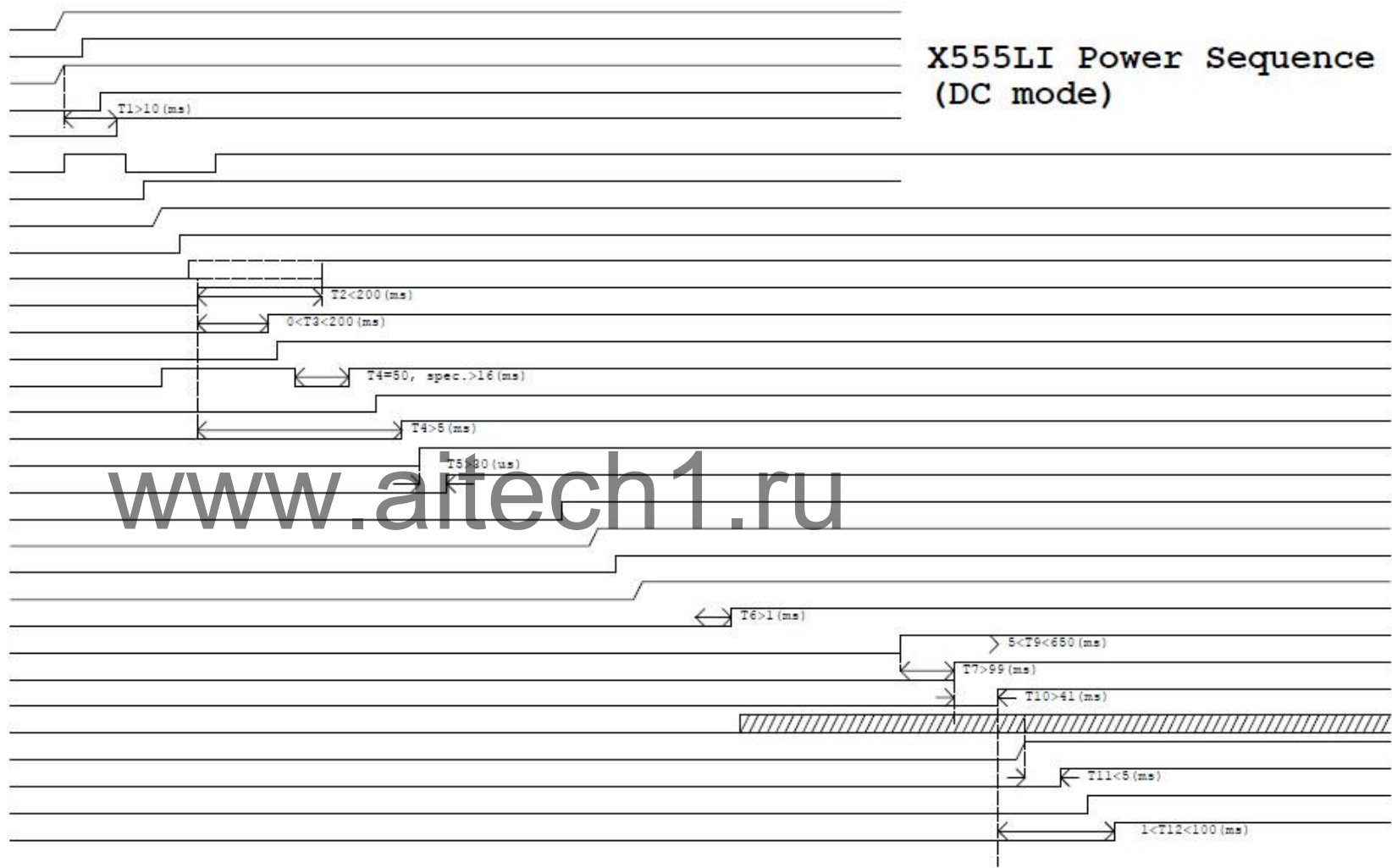


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# POWER ON SEQUENCE (DC Mode)

## DC-IN Mode

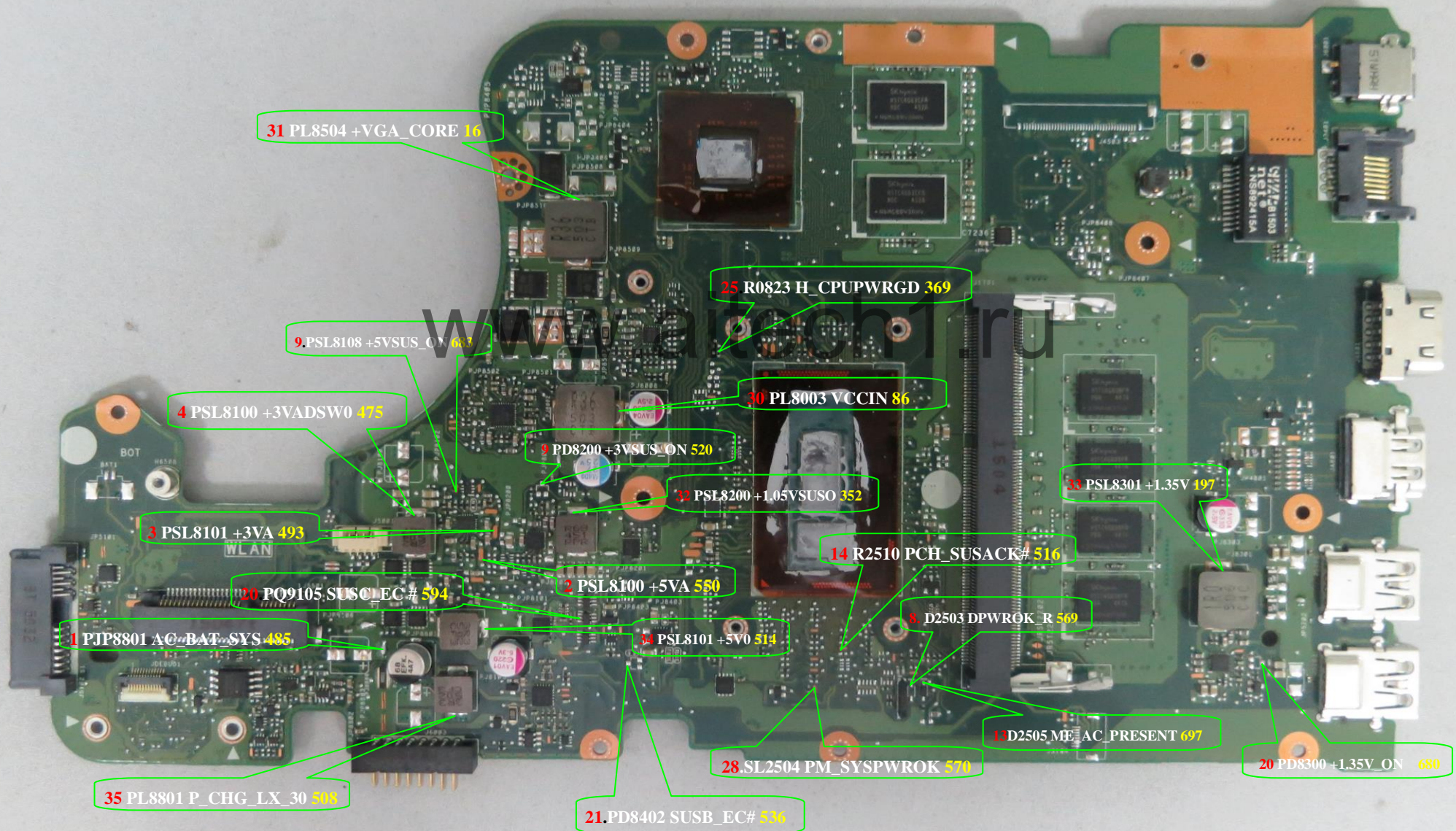
- 1 +3VA/+5VA/+3VA\_EC
- 2 EC\_RST#
- +3VA\_DSW
- 3 PCH\_SLPSUS#
- 4 DPWROK\_EC
- 5 PWR\_SW#
- 6 SVSUS\_ON/3VSUS\_ON
- +SVSUS/+3VSUS
- 7 SVSUS\_PWRGD/+3VSUS\_PWRGD
- 8 SUSWARN#
- 9 PM\_RSMRST#
- 10 ME\_AC\_PRESENT
- 11 PCH\_SUSACK#
- 12 PM\_PWRBTN#
- 13(a) SLP\_LAN#
- 13(b) PM\_SLP\_A#
- 14 PM\_SUSC#
- 15 PM\_SUSB#
- 16 SUSC\_EC#
- +5V/1.35V
- 17 SUSB\_EC#
- +5VS/+3VS/+1.8VS/+1.05VS/+0.675VS
- 19 PM\_PWROK
- 20 ALL\_SYSTEM\_PWRGD
- 21 PM\_PWROK\_PCH
- 22 H\_CPUPWRGD
- 23 VR\_SVID\_CLK/VR\_SVID\_DATA
- +VCCIN
- 25 CORE\_PWRGD
- 26 PM\_SYSPWROK
- 27 BUF\_PLT\_RST#



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## Signal Measure Point-Bottom





## Signal Measure Point-Top

